

WEST Search History

DATE: Monday, June 28, 2004

Hide?	Set Name	Query	Hit Count
	<i>DB=EPAB; PLUR=YES; OP=ADJ</i>		
<input type="checkbox"/>	L16	memory and area adj3 (8F\$)	0
<input type="checkbox"/>	L15	cell and area adj3 (8F\$)	0
	<i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i>		
<input type="checkbox"/>	L14	L11 and area adj3 (8F\$)	5
<input type="checkbox"/>	L13	L12	0
<input type="checkbox"/>	L12	L11 and area ajd (8F\$)	0
<input type="checkbox"/>	L11	L10 and load	297
<input type="checkbox"/>	L10	L9 and power supply	469
<input type="checkbox"/>	L9	L8 and transistor	765
<input type="checkbox"/>	L8	L7 and PMOS	766
<input type="checkbox"/>	L7	L6 and NMOS	984
<input type="checkbox"/>	L6	L3 and switching adj1 (transistor or device)	4321
<input type="checkbox"/>	L5	L3 and switching ajd1 (transistor or device)	0
<input type="checkbox"/>	L4	L3 and switching device	1601
<input type="checkbox"/>	L3	memory cell	58289
<input type="checkbox"/>	L2	memory cell	58289
<input type="checkbox"/>	L1	memory	624736

END OF SEARCH HISTORY

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